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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/710,057	
	Filing Date	10 Nov 2000	
	First Named Inventor	Becky Cavanaugh	
	Art Unit	2184	
	Examiner Name	Damiano, Anno L.	
Total Number of Pages in This Submission	22	Attorney Docket Number	219205.00002

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Raman N. Dewan, Jackson Walker L.L.P.
Signature	<i>R. N. Dewan</i>
Date	08 Jan 04

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: **Cavanaugh et al.**

Serial No.: **09/710,057**

Filed: **11/10/2000**

For: **Method and Apparatus that  
Simulates the Execution of Parallel  
Instructions in Processor  
Functional Verification Testing**

§ Group Art Unit: 2184

§ Examiner: **Damiano**

§ Attorney Docket: **219205.00002**

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**RESPONSE TO OFFICE ACTION DATED OCTOBER 10, 2003**

MAIL STOP NON-FEE AMENDMENT  
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Dear Sir:

This paper is submitted in response to the office action dated October 10, 2003, in the above-referenced patent application (the "Office Action") after careful consideration to address the issues raised therein and to highlight why the case is believed to be in condition for allowance.

**Amendments to the Drawings** are reflected on page 2 of this paper.

**Amendments to the Specification** are reflected on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 17 of this paper.